# The RF Line UHF Power Amplifiers

... designed specifically for portable radio applications. The MHW804 Series is capable of wide power range control, operates from a 7.5 volt supply and requires only 1.0 mW of RF input power.

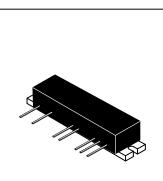
- MHW804–1 800 to 870 MHz MHW804–2 — 896 to 940 MHz
- Specified 7.5 Volt Characteristics: RF Input Power — 1.0 mW (0 dBm) RF Output Power — 4.0 W Minimum Gain — 36 dB Harmonics — -45 dBc Max @ 2.0 f<sub>0</sub>
- 50 Ohm Input/Output Impedances
- Guaranteed Stability and Ruggedness
- Circuit board photomaster available upon request by contacting RF Tactical Marketing in Phoenix, AZ.

## MAXIMUM RATINGS (Flange Temperature = 25°C)

| Rating                           | Symbol            | Value        | Unit |
|----------------------------------|-------------------|--------------|------|
| DC Supply Voltage                | Vs                | 10           | Vdc  |
| DC Control Voltage               | V <sub>cont</sub> | 4.0          | Vdc  |
| RF Input Power                   | Pin               | 5.0          | mW   |
| RF Output Power                  | Pout              | 6.0          | W    |
| Operating Case Temperature Range | тс                | - 30 to +100 | °C   |
| Storage Temperature Range        | T <sub>stg</sub>  | - 30 to +100 | °C   |



4.0 WATTS 800 to 940 MHz RF POWER AMPLIFIERS



CASE 301F-03, STYLE 1

**ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = + 25°C, 50 ohm system, unless otherwise noted)

| Characteristic                                                                                                                                                                                                                                                                                       |                                                                                         | Symbol             | Min                                                             | Max                | Unit |
|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------------------|--------------------|-----------------------------------------------------------------|--------------------|------|
| Frequency Range                                                                                                                                                                                                                                                                                      | MHW804–1<br>MHW804–2                                                                    | BW                 | 800<br>896                                                      | 870<br>940         | MHz  |
| Power Gain ( $V_{s1} = V_{s2} = V_{s3} = V_{s4} = V_{s5} = 7.5 \text{ V}; V_{cont} = 3.75 \text{ V}$ )                                                                                                                                                                                               |                                                                                         | Gp                 | 36                                                              | —                  | dB   |
| Control Voltage ( $P_{in} = 0 \text{ dBm}$ , $P_{out} = 4.0 \text{ W}$ , $V_{s1} = V_{s2} = V_{s3} = V_{s4} = V_{s5} = 7.5 \text{ V}$ ,<br>Adjust $V_{cont}$ for specified $P_{out}$ )                                                                                                               |                                                                                         | V <sub>cont</sub>  | _                                                               | 3.75               | Vdc  |
| Efficiency (Same condition as for V <sub>cont</sub> )                                                                                                                                                                                                                                                |                                                                                         | η                  | 32                                                              | —                  | %    |
| Current Drain (Same conditions as for $V_{\mbox{cont}})$                                                                                                                                                                                                                                             | IS1 + IS4 (Pins 2, 5)<br>IS2 + IS3 + IS5 (Pins 3, 4, 6)<br>I <sub>control</sub> (Pin 1) | ۱ <sub>D</sub>     |                                                                 | 210<br>1430<br>0.2 | mA   |
| Input VSWR (Same conditions as for $V_{\text{cont}}$ )                                                                                                                                                                                                                                               |                                                                                         | VSWR <sub>in</sub> | —                                                               | 2.0:1              | —    |
| Harmonic Content (Same conditions as for $V_{\text{cont}}$ )                                                                                                                                                                                                                                         | 2.0 f <sub>0</sub><br>3.0 f <sub>0</sub>                                                | —                  | _                                                               | - 45<br>- 50       | dBc  |
| Leakage Current — $I_{S2} + I_{S3} + I_{S5}$ ( $V_{S2} = V_{S3} = V_{S5} = 7.5$ V; $V_{S1} = V_{S4} = 0$ V<br>V <sub>cont</sub> = 0 V; P <sub>in</sub> = 0 mW)                                                                                                                                       |                                                                                         | ١Ľ                 | _                                                               | 0.3                | mA   |
| Standby Current — $I_{S1} + I_{S4}$ ( $V_{S1} = V_{S2} = V_{S3} = V_{S4} = V_{S5} = 7.5$ V<br>V <sub>cont</sub> = 4.0 V; P <sub>in</sub> = 0 mW)                                                                                                                                                     |                                                                                         | IS                 | —                                                               | 220                | mA   |
| Load Mismatch Stress ( $V_{S1} = V_{S2} = V_{S3} = V_{S4} = V_{S5} = 9.0 V$ ;<br>$P_{in} = 2.0 \text{ mW}$ ; $P_{out} = 6.0 \text{ W}$ ; Load VSWR = 20:1, All Phase Angles.<br>Adjust $V_{cont}$ for Specified $P_{out}$ )                                                                          |                                                                                         | Ψ                  | No Degradation<br>in Output Power                               |                    |      |
| Stability (V <sub>S1</sub> = V <sub>S2</sub> = V <sub>S3</sub> = V <sub>S4</sub> = V <sub>S5</sub> = 6.0 to 9.0 V; P <sub>IN</sub> = $-1.0$ dBm to $+3.0$ dBm;<br>P <sub>out</sub> = 1.0 W to 4.0 W; Load VSWR = 6:1, All Phase Angles;<br>Adjust V <sub>cont</sub> for Specified P <sub>out</sub> ) |                                                                                         | —                  | All Spurious Outputs<br>More Than 60 dB Below<br>Desired Signal |                    |      |



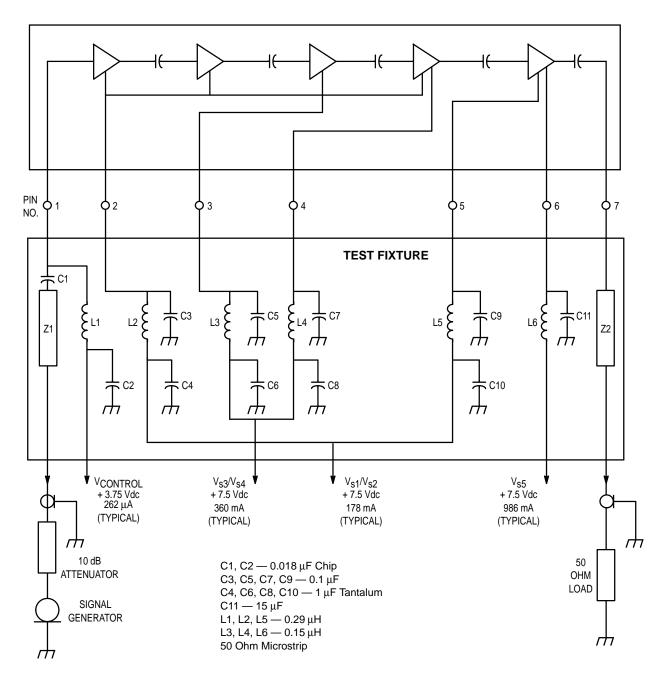
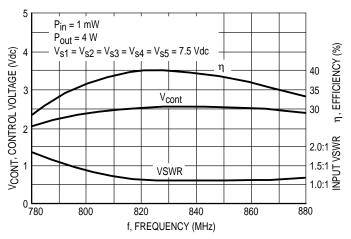
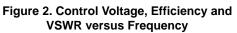


Figure 1. Power Module Test System Block Diagram

## **TYPICAL CHARACTERISTICS**





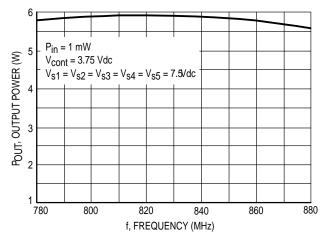


Figure 4. Output Power versus Frequency

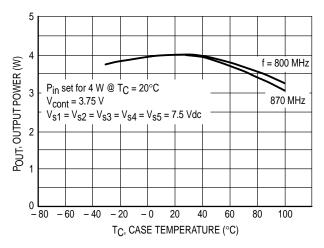


Figure 6. Output Power versus Case Temperature

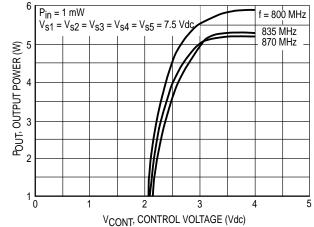


Figure 3. Output Power versus Control Voltage

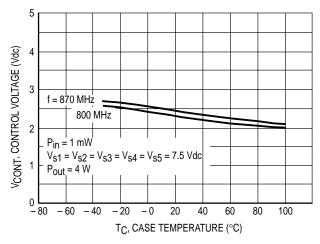
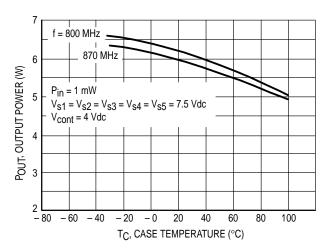
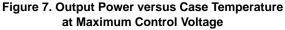


Figure 5. Control Voltage Case Temperature





## **APPLICATIONS INFORMATION**

#### NOMINAL OPERATION

All electrical specifications are based on the nominal conditions of  $V_{S1} = V_{S2} = V_{S3} = V_{S4} = V_{S5} = 7.5$  Vdc (Pins 2, 3, 4, 5, 6) and P<sub>out</sub> equal to 4.0 watts. With these conditions, maximum current density on any device is  $1.5 \times 10^5$  A/cm<sup>2</sup> and maximum die temperature with 100°C case operating temperature is 165°C. While the modules are designed to have excess gain margin with ruggedness, operation of these units outside the limits of published specifications is not recommended unless prior communications regarding intended use have been made with the factory representative.

### **GAIN CONTROL**

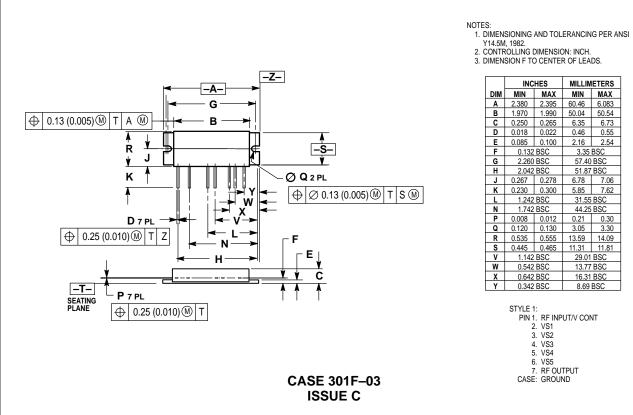
The module output should be limited to 4.0 watts. The preferred method of power output control is to fix  $V_{S1} = V_{S2} = V_{S3}$ =  $V_{S4} = V_{S5} = 7.5$  Vdc (Pins 2, 3, 4, 5, 6), P<sub>in</sub> (Pin 1) at 1.0 mW, and vary  $V_{cont}$  (Pin 1) voltage.

#### DECOUPLING

Due to the high gain of the three stages and the module size limitation, external decoupling networks require careful consideration. Pins 2, 3, 4, and 6 are internally bypassed with a 0.018  $\mu$ F chip capacitor which is effective for frequencies from 5.0 MHz through 925 MHz. For bypassing frequencies below 5.0 MHz, networks equivalent to that shown in Figure 1 are recommended. Inadequate decoupling will result in spurious outputs at certain operating frequencies and certain phase angles of input and output VSWR.

### LOAD MISMATCH

During final test, each module is load mismatch tested in a fixture having the identical decoupling networks described in Figure 1. Electrical conditions are  $V_{S1} = V_{S2} = V_{S3} = V_{S4} = V_{S5}$  equal to 9.0 V, VSWR equal to 20:1, and output power equal to 6.0 watts.



|     | INCHES    |           | MILLIMETERS |           |  |  |
|-----|-----------|-----------|-------------|-----------|--|--|
| DIM | MIN       | MAX       | MIN         | MAX       |  |  |
| Α   | 2.380     | 2.395     | 60.46       | 6.083     |  |  |
| В   | 1.970     | 1.990     | 50.04       | 50.54     |  |  |
| С   | 0.250     | 0.265     | 6.35        | 6.73      |  |  |
| D   | 0.018     | 0.022     | 0.46        | 0.55      |  |  |
| E   | 0.085     | 0.100     | 2.16        | 2.54      |  |  |
| F   | 0.132     | BSC       | 3.35 BSC    |           |  |  |
| G   | 2.260 BSC |           | 57.40 BSC   |           |  |  |
| Н   | 2.042 BSC |           | 51.87 BSC   |           |  |  |
| J   | 0.267     | 0.278     | 6.78        | 7.06      |  |  |
| K   | 0.230     | 0.300     | 5.85        | 7.62      |  |  |
| L   | 1.242     | BSC       | 31.55       | BSC       |  |  |
| N   | 1.742     | 1.742 BSC |             | 44.25 BSC |  |  |
| Р   | 0.008     | 0.012     | 0.21        | 0.30      |  |  |
| Q   | 0.120     | 0.130     | 3.05        | 3.30      |  |  |
| R   | 0.535     | 0.555     | 13.59       | 14.09     |  |  |
| S   | 0.445     | 0.465     | 11.31       | 11.81     |  |  |
| V   | 1.142     | 1.142 BSC |             | 29.01 BSC |  |  |
| W   | 0.542     | BSC       | 13.77 BSC   |           |  |  |
| X   | 0.642     | BSC       | 16.31 BSC   |           |  |  |
| Y   | 0.342 BSC |           | 8.69 BSC    |           |  |  |

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